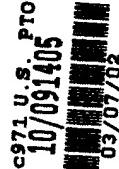


JP971 U.S. PTO
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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10091405	FILING DATE 03/07/2002	CLASS 349	SUBCLAS\$ 263	GAU 147	2838 201	EXAMINER SHAPIRO <i>Matt</i> <i>Name</i>
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Uchida Takaoshi; Ookado Kazuo; Yokota Yoshikazu;
363 147 2838

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-110813 04/10/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	H-1030	
Verified and Acknowledged Examiners's initials			
TITLE : Semiconductor integrated circuit with voltage generation circuit, liquid crystal display controller and mobile electric equipment			
U.S.DEP.T. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.	Print Fig.
TERMINAL DISCLAIMER		Application Examiner		
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